

09 June 2021

ERRATUM

(RFP No. 952/09/06/2021)

Request for Proposals (RFP) for the provision of FPGA and electronic hardware engineering services to the CSIR

On 02 June 2021, the Council for Scientific and Industrial Research (CSIR) invited suitably qualified and experienced service providers to submit proposals for the above-mentioned RFP. The original closing/submission date for the tender is 09 June 2021.

The CSIR appreciates your interest in responding to the Request for Proposal.

Please note the following changes in the advertised tender documents:

- The closing date has been extended to 11 June 2021 at 16:30.

The CSIR wishes to encourage you to submit a response to this tender, and to apologise for any inconvenience that might be caused.

Enquiries may be directed to tender@csir.co.za